

M 28224

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes et al.

Title:

TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND

USE

Docket No.:

303.324US2

Filed: Examiner: February 23, 1999 Michael Trinh Serial No.: 09/256,643

Due Date: April 14, 2000 Group Art Unit: 2822

**Assistant Commissioner for Patents** 

Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

X A return postcard.

X A Response to Restriction Requirement and Preliminary Amendment (14 Pages).

X A Supplemental Information Disclosure Statement (1 pgs.), Form 1449 (1 pgs.), and copies of 6 cited references.

X A check in the amount of \$984.00 to cover the fee for additional claims as calculated below.

If an additional fee is required due to changes to the claims, the fee has been calculated as follows:

		CL	AIMS AS AMENDE	D		
	(1) Claims Remaining After Amendment		(2) Highest Number Previously Paid For	(3) Present Extra	Rate	Fee
TOTAL CLAIMS	53	-	20	33	x 18 =	\$594.00
INDEPENDENT CLAIMS	8	-	3	5	x 78 =	\$390.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED						\$0.00
TOTAL						\$984.00

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described above, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this day of April, 2000.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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Atty: Robert E. Mates

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